



Paras Jain

E-Mail: x25parasj@iima.ac.in Mobile: +91 9811076965 LinkedIn: www.linkedin.com/paras-jain

Summary

Technology professional with 5+ years building scalable cloud security solutions at NetApp and networking products at Qualcomm. Proven track record of delivering customer-centric features and driving efficiency through cross-functional collaboration.

Professional Experience

Member of Technical Staff | NetApp India Pvt. Ltd. | Bengaluru

- Led backend API development for core product modules including risk analysis and compliance features, contributing to **20% expansion of product portfolio**.
- Architected and implemented** Auto Remediation, Azure Topology, and CloudTrail Events analysis features based on **direct customer requirements**, achieving multi-cloud support. (Majorly used **Python** and its frameworks)
- Enhanced user experience by **reducing API response time** for risk-based event insights **from 10+ seconds to under 5 seconds** through database and data structure optimization.
- Established **technical integration frameworks** across 20+ member multi-cultural team, enabling foundational work for Auto Remediation and Notifications features.
- Mentored backend developers** in technical design and code reviews, contributing to team capability building.
- Developed **on-demand ETL pipelines** using AWS services like Lambda, Step Functions, SQS, and CloudFormation for **real-time collection** of customer cloud account configurations.
- Broadened product security coverage and compliance capabilities** (SOC2, NIST, HIPAA) by developing and integrating automated Python-based security rules for AWS and Azure cloud providers.
- Designed an algorithm** for Azure Cloud Network Topology, **providing customers with actionable insights** into security architectures via graph visualization.
- Developed** custom requested feature: **the Reporting Engine**, delivering detailed risk profiles and compliance reports to **empower customers** with comprehensive cloud security insights.
- Managed AWS CodePipeline** optimization ensuring **seamless feature releases** across development, testing, and production environments.
- Authored Product Requirement and Engineering Planning Documents**, collaborating closely with product managers and cross-functional teams to align technical solutions with business objectives.

Engineer | Qualcomm India Pvt. Ltd. | Hyderabad, India

- Collaborated within a 30-member Advanced Routers team** on Snapdragon X55 chipsets to facilitate robust connectivity in 4G networks.
- Designed and implemented **customer-centric features** (e.g., Parental Control, Bandwidth Control, MACsec) **adopted by 20+ OEMs as flagship features** in mobile routers.
- Led end-to-end design and implementation** of crucial features, resolving and closing **over 100 customer queries** with successful fixes and code patches. (Mainly used C/C++ as programming languages)
- Improved memory efficiency in low-memory chipsets by 40%** by reducing memory leaks in the Soft Access Point (SoftAP) application service.
- Co-led the SDx65 chipset launch** for the SoftAP team in India, validating 20+ legacy features and completing Linux kernel upgradation to version 5.4.
- Directly **collaborated with customer engineering teams** to gather requirements and deliver agile, ad-hoc features, ensuring **high customer satisfaction**.
- Partnered closely** with platform engineering, integration, and QA teams for robust **product feature delivery** through rigorous checks and stress-tests.
- Independently managed feature ownership**, including **resolving critical hardware-software integration issues** in flagship products like Voice over IP Call crashes.
- Contributed to an initiative to streamline new hire onboarding**, developing comprehensive knowledge trainings that **reduced the training period from 2 months to 1 month**.

Education

Bachelor of Technology – *Indraprastha Institute of Information Technology, Delhi (IIIT-D)* | India

Accomplishments

- Recognized in the Top 25 out of 200+ teams in PM Dissect Competition conducted by IIM Ahmedabad.
- Granted company stocks and bonuses at NetApp as reward for cumulative performance.

- Received patent approval for a wearable device and the method of operating the device to perform multiple functions seamlessly as part of the project “Wear N Go”.
- Received Orion Insta Award in for outstanding contributions and efforts at Qualcomm.
- Granted company stocks and bonuses at Qualcomm as reward for performance.
- Promoted to Engineer position from Associate Engineer at Qualcomm.

Extra-Curriculars

- PGPX Placement Committee Vertical Head for Technology. Leading placement strategy and industry outreach for technology sector roles.
- Elected PGPX Secretary to IT, Infrastructure & Dorm Affairs Committee. Managed dorm issues, infrastructure and IT issues for 150+ PGPX students, coordinating with administration for service improvements.
- Volunteering at Teach for India, teaching 40 students of class IV at Nigam Pratibha Vidyalaya, Delhi.
- Completed German language certifications (A1, A2 levels).
- Served as member of the Theatre Club, Weave Design Club at IIIT-Delhi.